## **ARCHITECT'S DESIGN STATEMENT**

In respect of

## CyrusOne 110kv SUBSTATION

Prepared by

**TOT ARCHITECTS** 

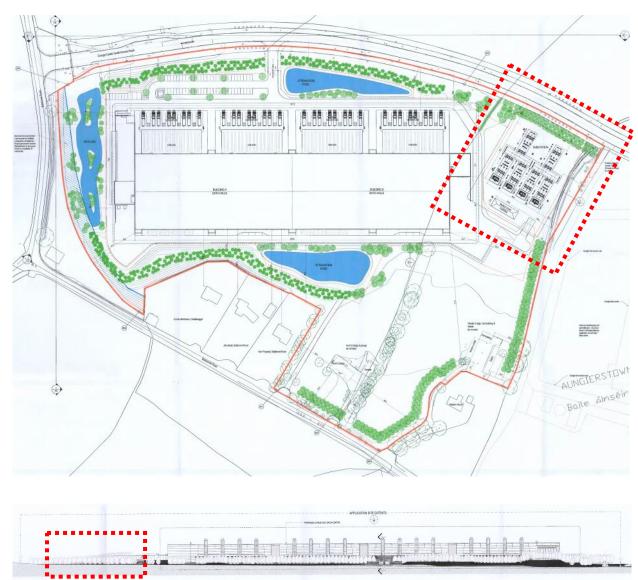
Job No: 19-128

Date: 15.12.2020

TOT ARCHITECTS 1

## **DESIGN RATIONALE**

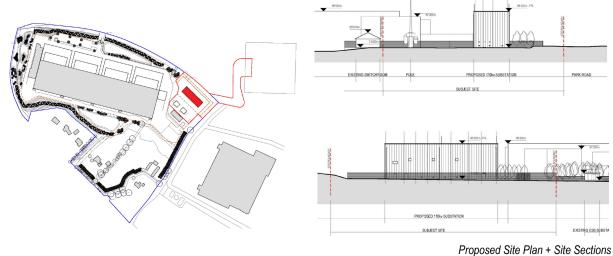
This design rationale has been prepared on behalf of CyrusOne and relates to the provision of a 110kv Substation located adjacent to previously granted CyrusOne Data Centre (planning reference SD18A/0134). The original grant provided for external electrical equipment. The intention of this planning application is to enclose some of that electrical equipment in a two storey substation building.

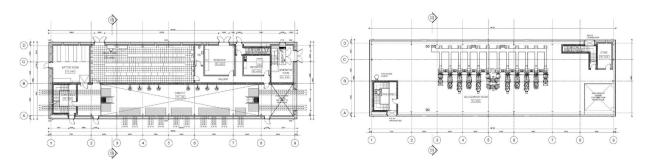


SD18A/0134 - Original Site Plan + Contiguous Elevation with proposed Substation site location outlined in RED

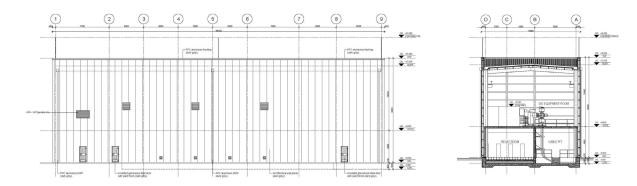
The proposed development provides for a 2 storey, 1,307sq.m 110kv Substation with associated ancillary external electrical plant. The building footprint will be 715.7sq.m with an overall height of 15.20m. The proposed façade treatment of insulated flat metal panels will be consistent with the immediate Grange Castle South context and wider Grange Castle area. These panels will be dark grey in colour and will have vertical jointing. Galvanised steel doors are located at ground level with powder coated louvres and vents as indicated on the proposed elevations. The accommodation consists of a Battery room, Relay room, Workshop, Messroom, Generator room and Cable Pit at ground level with GIS Equipment room and Store at first floor serviced via 2no. stairs. The external electrical plant consists primarily of Transformers and Lightning Poles as per the drawings submitted as part of this application. The site perimeter treatment will match that proposed in the original planning grant with details of the fencing and gates included in the landscape planning information. Access to the Substation compound is proposed via the Grange Castle South access road and from the main CyrusOne Data Centre site.

TOT ARCHITECTS 2





Proposed Layout Plans



Proposed Elevation + Section

**TOT** ARCHITECTS 3